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MEAN WELL

SWITCHING POWER SUPPLY

ISO-9001 CERTIFIED MANUFACTURER

DR-75 SERIES

Jameco Part Number 221866 (DR-75-48)

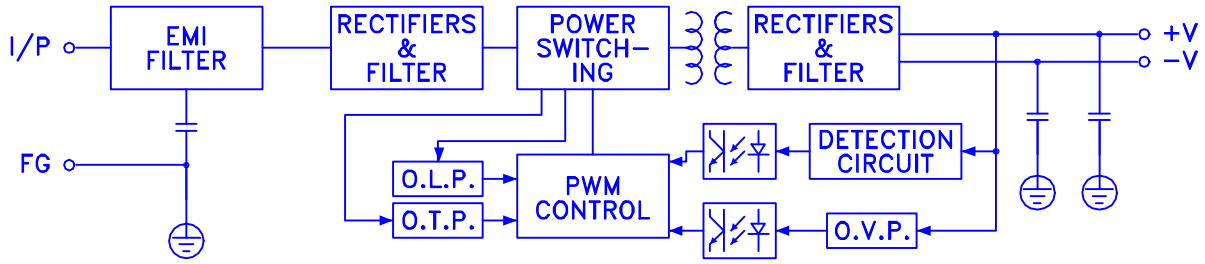
.LOW COST, HIGH RELIABILITY
.105°C OUTPUT CAPACITOR
.INTERNATIONAL AC INPUT RANGE
.HIGH EFFICIENCY, LOW WORKING TEMPERATURE
.SOFT-START CIRCUIT, LIMITING AC SURGE CURRENT
.SHORT CIRCUIT, OVERLOAD, OVER VOLTAGE PROTECTED
.COMPACT SIZE, LIGHT WEIGHT
.100% FULL LOAD BURN-IN TEST
.BUILT IN EMI FILTER, LOW RIPPLE NOISE

MODEL / SPECIFICATION	DR-75-12	DR-75-24	DR-75-48
DC OUTPUT VOLTAGE	12V	24V	48V
OUTPUT V. TOLERANCE	±2%	±1%	±1%
OUTPUT RATED CURRENT	6.3A	3.2A	1.6A
OUTPUT CURRENT RANGE	0-6.3A	0-3.2A	0-1.6A
RIPPLE & NOISE	100mVp-p	150mVp-p	240mVp-p
LINE REGULATION	±0.5%	±0.5%	±0.5%
LOAD REGULATION	±1%	±1%	±1%
DC OUTPUT POWER	76W	76.8W	76.8W
EFFICIENCY	76%	80%	81%
DC VOLTAGE ADJ.	12~14V	24~28V	48~53V
INPUT VOLTAGE RANGE	85~264VAC 47~63Hz; 120~370VDC		
AC CURRENT	2.0A/115V 1.2A/230V		
INRUSH CURRENT	COLD START 30A/115V 60A/230V		
LEAKAGE CURRENT	<1mA/240VAC		
OVERLOAD PROTECTION	105% ~150% TYPE:CONSTANT CURRENT LIMITING RESET:AUTO RECOVERY		
OVER VOLTAGE PROTECTION	15~16.5V	29~34V	58~65V
OVER TEMP. PROTECTION	85°C± 5°C TYPE:SHUTDOWN RESET:AUTO RECOVERY(TSW1)		
TEMP. COEFFICIENT	±0.03% / °C (0~50°C)		
SETUP, RISE, HOLD UP TIME	1000ms;60ms;50ms/230V;1800ms;60ms;10ms/115V		
VIBRATION	10~500Hz, 2G 10min./1cycle, PERIOD FOR 60min. EACH AXES		
WITHSTAND VOLTAGE	I/P-O/P:3KVAC I/P-FG:1.5KVAC O/P-FG:0.5KVAC		
ISOLATION RESISTANCE	I/P-O/P, I/P-FG, O/P-FG:500VDC / 100M Ohms		
WORKING TEMP., HUMIDITY	-10°C~+60°C(REFER TO OUTPUT DERATING CURVE), 20%~90% RH		
STORAGE TEMP., HUMIDITY	-20°C~+85°C, 10%~95% RH		
DIMENSION	55.5*125.2*100 mm (W*H*D)		
WEIGHT	0.55Kgs		
SAFETY STANDARDS	TUV EN60950, UL508 APPROVED		
EMC STANDARDS	EN55022 CLASS B, EN61000-4-2,3,4,5,6,8,11; ENV50204, EN61000-3-2,-3 VERIFICATION		

NOTE :
1.ALL PARAMETERS ARE SPECIFIED AT 230VAC INPUT, RATED LOAD, 25°C 70% RH. AMBIENT.
2.TOLERANCE : INCLUDE SET UP TOLERANCE, LINE REGULATION, LOAD REGULATION.
3.RIPPLE & NOISE ARE MEASURED AT 20MHz BY USING A 12" TWISTED PAIR TERMINATED WITH A 0.1uF & 47uF CAPACITOR.
4.LINE REGULATION IS MEASURED FROM LOW LINE TO HIGH LINE AT RATED LOAD.
5.LOAD REGULATION IS MEASURED FROM 0% TO 100% RATED LOAD.

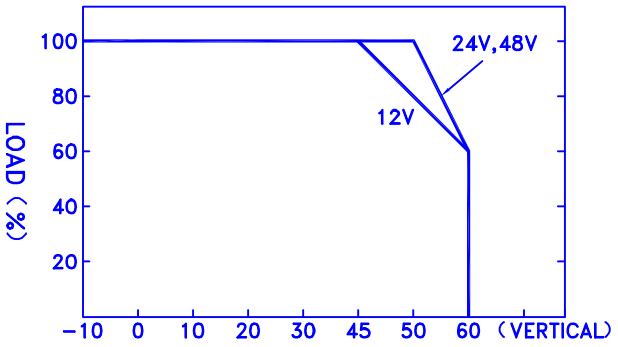
2002-09-16

BLOCK DIAGRAM

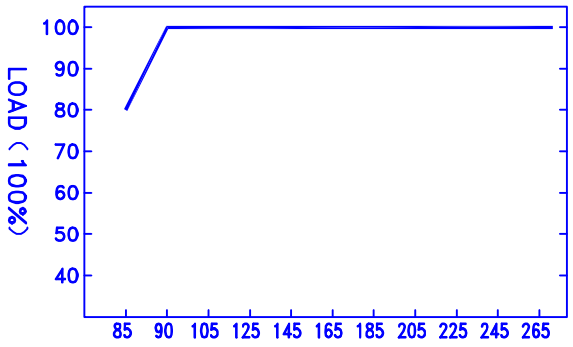


fosc：50KHz

OUTPUT DERATING

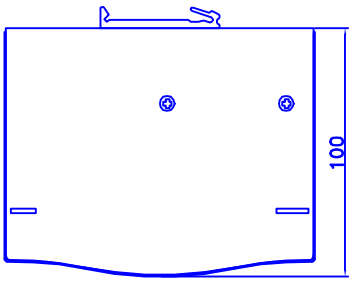


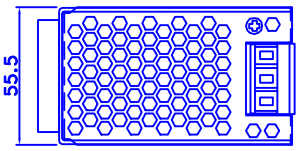
OUTPUT DERATING VS INPUT VOLTAGE

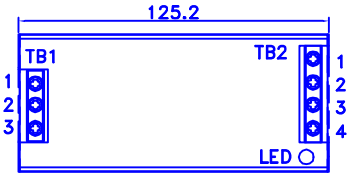


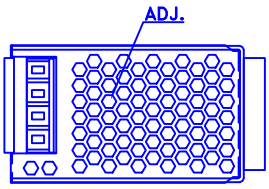
DIMENSION (mm)

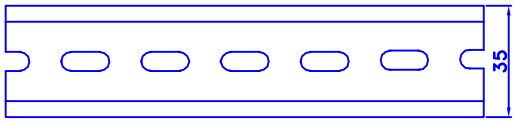
- CASE : 923
- TERMINAL PIN NO. ASSIGNMENT
  - TB1 : AC INPUT
    - PIN 1 : ⏚
    - PIN 2 : AC/N
    - PIN 3 : AC/L
  - TB2 : DC OUTPUT
    - PIN 1,2 : +V
    - PIN 3,4 : -V











ADMISSIBLE DIN-RAIL:TS35/7.5 OR TS35/15